Prototype Testing Lab Results for INA116 Instrumentation Amplifier

This document provides an overview of our lab test results with INA116 Instrumentation Amplifier. Our goal is to obtain accurate pH measurement using an instrumental amplifier.

[Figure 1](#page-0-0) below shows the schematic of the circuit used to obtain results in Test-1, Test-2 & Test-3. We connected two resistors 10 MΩ in series with amplifier input (Pin 6) V_{in+} to simulate the ideal *pH* electrode and 47 kΩ in series with amplifier input (Pin 3) V_{in} to simulate the ideal reference electrode. Decoupling 0.1 μ F capacitor were connected to both power rails (Pins 8 and 13) V_{+}/V_{-} to provide stability and minimize any ripple in the power supply. Also 0.1µF capacitor connects both amplifier inputs together to provide RFI protection.

Test 1

In order for us to confirm the proper operation of this INA chip, we set up the circuit as shown in the schematic above and set the differential input voltages to different values while observing the output in order to calculate our gain. Just for testing purposes, we chose a gain of 10 and as shown below in Table 1, the achieved gains were consistent while also being close to our desired gain. The figures below the table are the graphs of the approximate differential outputs on an oscilloscope based on the differential inputs.

 $Gain(Av)_{desired} = 10 V/V$

 $Gain(Av)_{Formula} = 1 +$ 50 kΩ Rg

 $Rg_{required} = 1 +$ 50kΩ $\frac{\partial^2 \theta}{\partial G(\text{Av})} = 5.555 k\Omega$

 $Rg_{\text{Picked}} = 5.486 k\Omega \pm 5\%$ tolerance

 $\text{Gain(Av)}_{\text{Calculated per Rg picked value}} = 1 +$ 50 kΩ 5.486 kΩ $= 10.11 V/V$

Table 1

Figure 4

Test 2

 $Gain(Av)_{desired} = 11.51 v/v$ [NOTE: Gain value chosen per Dr. Hintz AD8222 Instrumental Amplifier **prototype as its set to 11.51 V/V]**

 $Gain(Av)_{Formula} = 1 +$ 50 kΩ Rg $Rg_{required} = 1 +$ 50kΩ $\frac{66.04}{G(Av)} = 4.757 k\Omega$

 $Rg_{\text{Picked}} = 4.764 k\Omega \pm 5\%$ tolerance

 $\text{Gain(Av)}_{\text{Calculated per Ry picked value}} = 1 +$ 50 kΩ 4.764 kΩ $= 11.49 V/V$

During our testing with the first INA116 chip we observed that our gain decreased as the input voltage increased. We want our gain to be constant as input voltage is varied therefore; we decided to switch that chip with another INA116 chip to verify if the chip is operational or not. Our test 3 results confirmed that INA116 chip used during test 2 was not working. We were able to obtain a constant gain output per variation in input voltage.

Table 2

Test 3

 $Gain(Av)_{desired} = 11.51 V/V [NOTE: Gain value chosen per Dr. Hintz AD8222 Instrumental Amplifier]$ **prototype as its set to 11.51 V/V]**

 $Gain(Av)_{Formula} = 1 +$ 50 kΩ Rg $Rg_{required} = 1 +$ 50kΩ $\frac{66.04}{G(Av)} = 4.757 k\Omega$

 $Rg_{\text{Picked}} = 4.764 k\Omega \pm 5\%$ tolerance

Gain(Av)<sub>Calculated per Rg picked value =
$$
1 + \frac{50 \text{ k}\Omega}{4.764 \text{ k}\Omega} = 11.49 \text{ V/V}
$$</sub>

Table 3

INA116 Instrumentation Amplifier Test

Below are the oscilloscope results based on the table above.

Figure 7

Figure 8

Figure 9

INA116 Instrumentation Amplifier Test

Figure 11

Test 4

In test 4, we used the *pH* probe from Dr Hintz's prototype as our differential input to the INA116 instrumentation amplifier through which we injected a series of buffer fluids (pH 4.0, 7.0, and 10.0). The result as shown in [Table 4](#page-8-0) was displayed on the oscilloscope in millivolts which corresponded to the pH value of the buffer solution. For this test, the amplifier was enclosed inside a faraday shielded box and we used a low tolerance resistor to set the gain

 $Gain(Av)_{desired} = 11.51 V/V [NOTE: Gain value chosen per Dr. Hintz AD8222 Instrumental Amplifier]$ **prototype as its set to 11.51 V/V]**

 $Gain(Av)_{Formula} = 1 +$ 50 kΩ Rg

 $Rg_{required} = 1 +$ 50kΩ $\frac{66.04}{G(Av)} = 4.757 k\Omega$

 $Rg_{\text{Picked}} = 4.7469 k\Omega \pm 0.1\%$ tolerance

$$
Gain(Av)_{Calculated\ per\ Rg\ picked\ value} = 1 + \frac{50 \ k\Omega}{4.7469 k\Omega} = 11.53 \ V/V
$$

pH **measurement results shown below**

Table 4

Figure 15

Test 5

In test 5, we were able to show the *pH* measurement capability of the INA116 instrumentation amplifier. We used the *pH* probe as our input to the amplifier through which we injected a series of buffer fluids (*pH* 4.0, 7.0, and 10.0). We then connected our instrumentation amplifier to the marine alkalinity measurement device prototype that Dr. Kenneth J. Hintz (Associate professor at George Mason University) built. The GUI (implemented by Dr. Hintz) displayed *pH* values and the voltage between the *pH* and reference electrodes which were read by analog-to-digital converter. The gain was set to 11.53 V/V similar to that of test 4 and the amplifier was enclosed within a faraday shielded box.

[Figure](#page-10-0) 17 below shows the results of the *pH* 4 buffer solution. For this case, the output on the GUI shows a *pH* of 3.97 and 113 mV. In an ideal situation, we would obtain +177 mV at a *pH* of 4.

[Figure 18](#page-11-0) below shows the results of the *pH* 7 buffer solution. The output on the GUI shows 6.30 *pH* and -20 mV. In an ideal situation, we would obtain 0 mV at a *pH* 7. During the test, we noticed that the response time for the *pH* probe was slow. This can be due to the buffer solution contaminated with other solutions over time. More testing will be done with new *pH* probe and new *pH* buffer solutions to get more accurate results.

[Figure 19](#page-12-0) below shows the results of the *pH* 10 buffer solution. The display on the GUI shows a *pH* of 9.2 and - 186.29 mV. In an ideal situation we would obtain -177.48 mV at a *pH* of 10. During the test we noticed that the response time for the *pH* probe was slow as in the *pH* 7 buffer test. It's possible that the buffer solution got contaminated with other solutions over time. More testing will be done with new pH probe and new pH buffer solutions to get more accurate results.

Implementation of faraday box

As shown below, we will be using this faraday box with shielded cables during our testing of the amplifier to block any static or non-static electric fields by channeling electric charge buildup on the aluminum foil to the ground. This will protect the internal components such as amplifier and signal conductor in the wire from external electrical noise and prevent RF signals from leaking into the signal.

Parts used:

- 1. Pre-galvanized steel box enclosure
- 2. Aluminum Foil Tape
- 3. Breadboard
- 4. Twinaxial 22-Guage stranded shielded cables
- 5. Heat shrink

Pending Task

- 1. Design of PCB board for INA116 Instrumental Amplifier
	- a. Our testing was done with INA116 chip placed on a breadboard. Per data sheet instructions on page 8, [Figure 24](#page-16-0) below shows PCB board layout for input guard pins. The data sheet states "*Careful circuit board layout and assembly techniques are required to achieve the exceptionally low input bias current performance of the INA116. Guard terminals adjacent to both inputs make it easy to properly guard the critical input terminal layout. Since traces are not required to run between device pins, this layout is easily accomplished, even with the surface mount package. The guards should completely encircle their respective input connections. Both sides of the circuit board should be guarded, even if only one side has an input terminal conductor. Route any time varying signals away from the input terminals. Solder mask should not cover the input and guard traces since this can increase leakage*".

2. More testing with the new pH probe [\(Figure 25\)](#page-16-1) and Buffer solutions [\(Figure 26\)](#page-17-0)

- 3. Implementation of Calibration procedure in GUI
- 4. Implementation/testing of RFI and Antialiasing Filter as shown in [Figure 27](#page-18-0) below

$$
\text{LP Filter}_{Diff} = \frac{1}{2\pi R(2C_D + C_C)}
$$

Note: $C_D \geq C_C$

RF rectification can cause some problems in an amplifier due to strong RF signal. Sometimes the disturbances can appear as a small DC offset voltages in the signal. By using the first low pass filter at the input stage we will be able to filter out very high frequencies. In our design we are only dealing with a 1 Hz input frequency so our filter will be designed with a cutoff frequency of 2 Hz. A second low pass filter will be implemented if necessary.

5. Implementation of new Faraday box using aluminum enclosure as shown in [Figure 28](#page-19-0) below

Figure 28

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